

Abstraction of Clocks in Synchronous Data-flow Systems

A. Cohen ¹ L. Mandel ² F. Plateau ² M. Pouzet²³

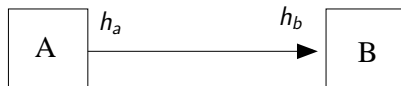
(1) INRIA Saclay - Ile-de-France, Orsay, France

(2) LRI, Univ. Paris-Sud 11, Orsay, France and INRIA Saclay

(3) Institut Universitaire de France

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N-Synchronous Model: A Relaxed Notion of Synchrony

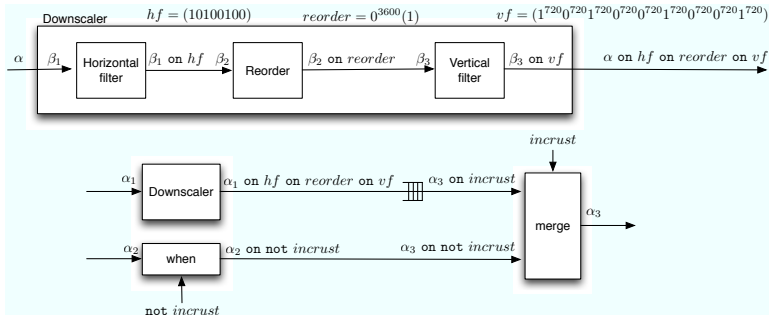


- ▶ **0-synchronous** : no buffers ($h_a = h_b$).
For instance $(1010) \neq (0110)$.
- ▶ **n-synchronous** : buffer of size n ($h_a <: h_b$).
For instance $(1010) <: (0110)$

Interest:

more flexible, as much guaranties as in synchronous model.

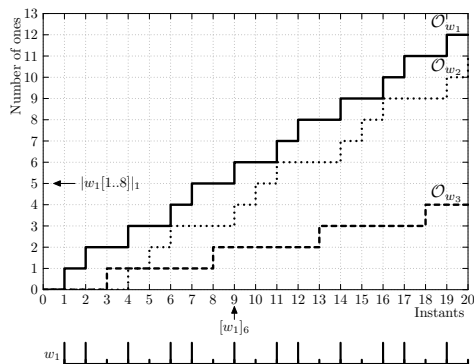
N-Synchronous Model: picture in picture example



- ▶ Previous work: subtyping relation can be checked provided clocks are periodic.
- ▶ Motivations:
 1. dealing with long patterns in periodic clocks
 2. modeling jitter

How to deal with “almost periodic” clocks? For instance α on w with $w = 00.((10) + (01))^\omega$
 (e.g. $w = 00\ 01\ 10\ 01\ 01\ 10\ 01\ 10\dots$)

Clocks as Infinite Binary Words



$$w ::= 0.w \mid 1.w$$

Notations:

$w[i]$: element at index i

$[w]_j$: position of the j^{th} 1

$O_w(i)$: number of 1s seen in w until index i .

buffer size $(w_1, w_2) = \max_{i \in \mathbb{N}} (O_{w_1}(i) - O_{w_2}(i))$

precedence $w_1 \preceq w_2 \stackrel{\text{def}}{\Leftrightarrow} \forall i, O_{w_1}(i) \geq O_{w_2}(i)$

synchronizability $w_1 \bowtie w_2 \stackrel{\text{def}}{\Leftrightarrow} \exists b_1, b_2 \in \mathbb{Z}, \forall i, b_1 \leq O_{w_1}(i) - O_{w_2}(i) \leq b_2$

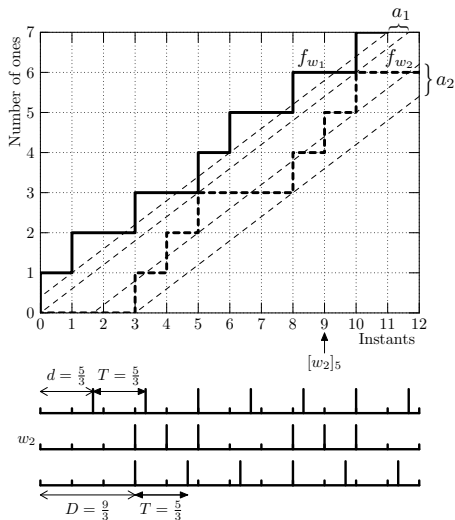
subtyping $w_1 <: w_2 \stackrel{\text{def}}{\Leftrightarrow} (w_1 \preceq w_2) / (w_2 \preceq w_2)$

composed clocks $c ::= w \mid \text{not } w \mid \text{con } c$

Abstraction of Infinite Binary Words [SYNCHRON'07, APLAS'08]:

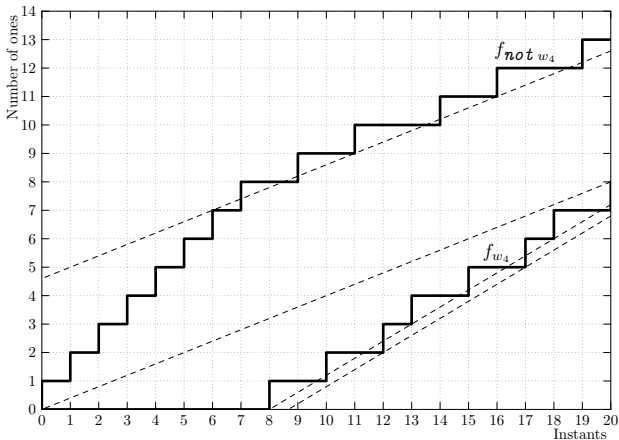
$$\text{abs}(w) = [d, D](T)$$

$$\text{concr}([d, D](T)) \stackrel{\text{def}}{\Leftrightarrow} \{w, \forall j \geq 0, T \times j + d \leq [w]_{j+1} \leq T \times j + D\}$$



Drawbacks

- ▶ sets of 1s in prefix are badly abstracted.



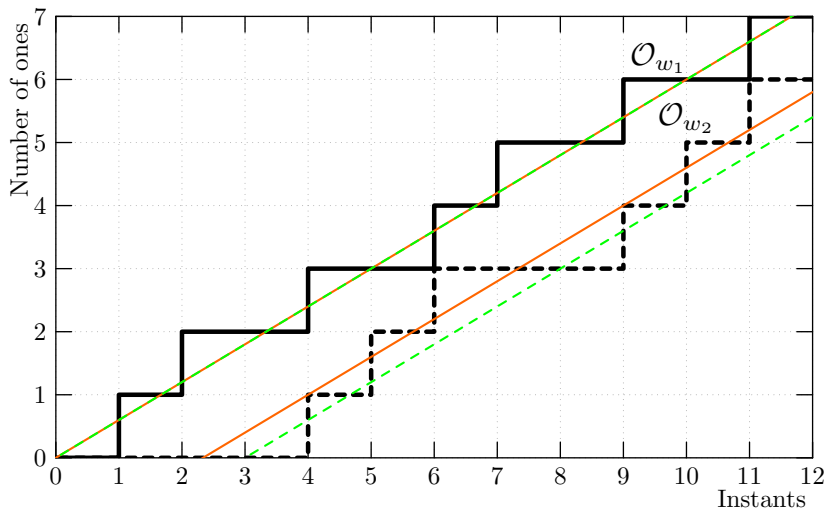
$$\Rightarrow a \subseteq not^{\sim} not^{\sim} a.$$

- ▶ words with finite number number of 1s cannot be abstracted.
 $\Rightarrow not^{\sim}$ can not be applied to $000(1)$.

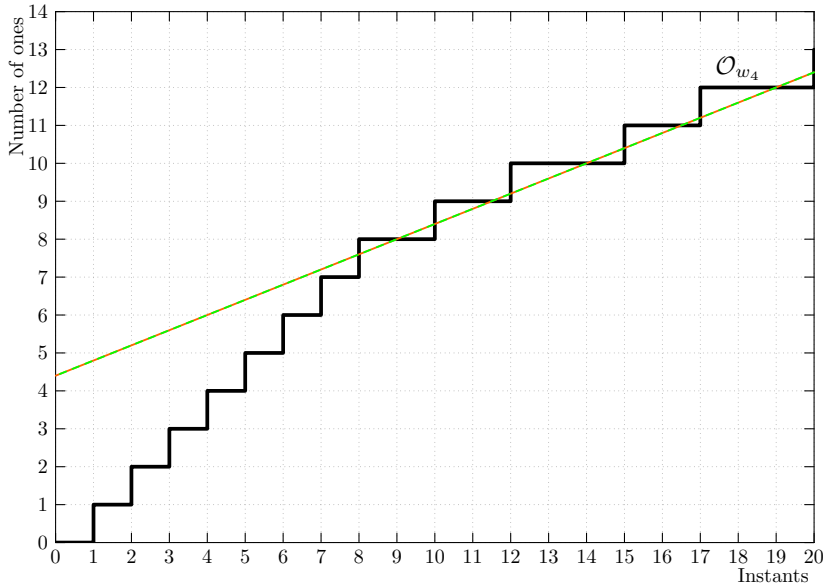
New Abstraction [JFLA'09]:

$\text{concr}((b^0, b^1, r)) \stackrel{\text{def}}{\Leftrightarrow}$

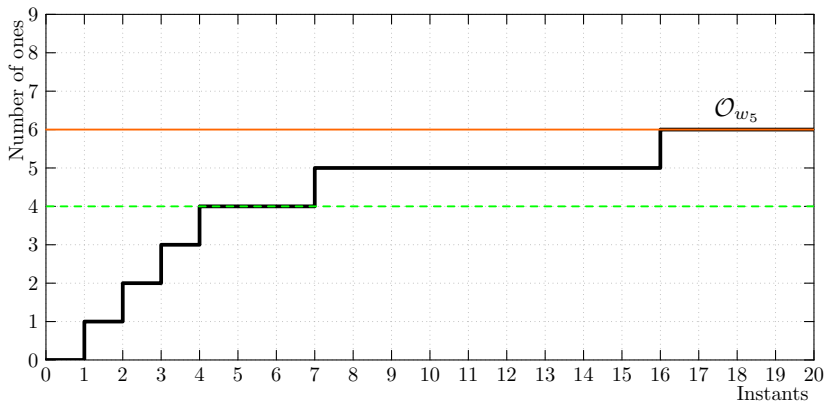
$$\left\{ w, \forall i \geq 1, \quad \begin{array}{l} w[i] = 1 \Rightarrow \mathcal{O}_w(i-1) < r \times i + b^1 \\ \wedge \quad w[i] = 0 \Rightarrow \mathcal{O}_w(i-1) \geq r \times i + b^0 \end{array} \right\}$$



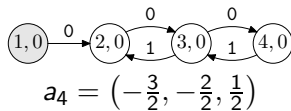
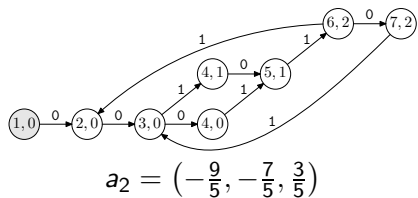
- ▶ Initial sets of 1s are well abstracted.



- ▶ Clocks with a nul rate can be abstracted.



Abstract Clocks as Automata



$A_a = \langle Q, \Sigma, \delta, q_o \rangle$ with:

- ▶ The set of states $Q = \{(i, j) \in \mathbb{N}^2\}$: coordinates in the 2D-chronograms.

Finite number of state equivalence classes.

- ▶ The initial state q_o is $(0, 0)$

- ▶ The alphabet Σ is $\{0, 1\}$

- ▶ The transition function δ is defined by:

$$\delta(1, (i, j)) = nf(i + 1, j + 1) \text{ if } \mathcal{O}_w(i - 1) < r \times i + b^1$$

$$\delta(0, (i, j)) = nf(i + 1, j) \text{ if } \mathcal{O}_w(i - 1) \geq r \times i + b^0$$

It is undefined otherwise.

Checking Clocks are in an Abstraction

```
type rat = { num: int; den: int; }
(* "+:", "<:" ... are operations over rat *)

let norm { num = n; den = 1; } i j =
  if i >= 1 && j >= n
  then (i - 1, j - n)
  else (i, j)

let node check (b0, b1, r) clk = ok where
  rec i, j = (1,0) fby norm r (i+1) (if clk then j + 1 else j)
  and ok =
    if clk
    then (rat_of_int j) <: r *: (rat_of_int i) +: b1
    else (rat_of_int j) >=: r *: (rat_of_int i) +: b0
```

Generating Clocks in an Abstraction

```
let node generate choice (b0, b1, r) = clk where
  rec i, j = (1,0) fby norm r (i+1) (if clk then j + 1 else j)
  and one = (rat_of_int j) <: (r *: (rat_of_int i) +: b1)
  and zero = (rat_of_int j) >=: (r *: (rat_of_int i) +: b0)
  and clk = choice one zero
```

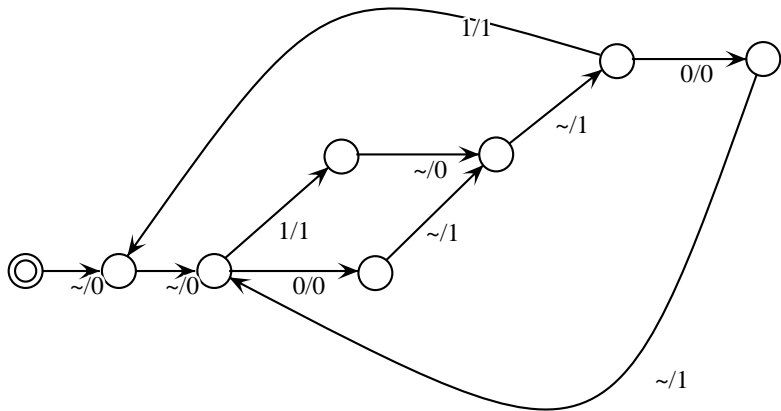
```
let node early a = generate (fun x y -> x) a
let node late a = generate (fun x y -> not y) a
```

Generating Clocks in an Abstraction with Lustre Compiler (Pascal Raymond)

```
node generate(x : bool) returns (clk: bool);
let
  -- we simply copy the input on the output
  clk = x ;
  -- but we enforce the correctness of x
  assert check(-9, -7, 3, 5, x) ;
tel

node early() returns (clk: bool);
let
  clk = generate(true);
tel

node late() returns (clk: bool);
let
  clk = generate(false);
tel
```



Abstract Operators:

- ▶ abstract *not* operator:

$$\text{not}^{\sim} ((b^0, b^1, r)) = (-b^1 - \varepsilon, -b^0 - \varepsilon, 1 - r)$$

$$\text{with } r = \frac{n}{l}, \quad b^0 = \frac{k^0}{l}, \quad b^1 = \frac{k^1}{l}, \quad \varepsilon = \frac{l-1}{l}$$

$$\text{Property: } a = \text{not}^{\sim} \text{not}^{\sim} a$$

- ▶ abstract *on* operator:

$$(b^0_1, b^1_1, r_1) \text{on}^{\sim} (b^0_2, b^1_2, r_2) = (b^0_{12}, b^1_{12}, r_{12})$$

with:

$$r_{12} = r_1 \times r_2,$$

$$b^0_{12} = b^0_1 \times r_2 + b^0_2,$$

$$b^1_{12} = b^1_1 \times r_2 + b^1_2 + \varepsilon.$$

Abstraction of a composed clock

► $abs(c)$

$$abs(not\ w) \stackrel{def}{\Leftrightarrow} not \sim abs(w)$$

$$abs(c_1\ on\ c_2) \stackrel{def}{\Leftrightarrow} abs(c_1)\ on \sim abs(c_2)$$

Proposition: $c \in concr(abs(c))$

Abstract Relations

- ▶ synchronizability: $(b^0_1, b^1_1, r_1) \bowtie^{\sim} (b^0_2, b^1_2, r_2) \Leftrightarrow r_1 = r_2$

Proposition: $abs(c_1) \bowtie^{\sim} abs(c_2) \Leftrightarrow c_1 \bowtie c_2$

- ▶ precedence: $b^0_1 \geq b^1_2 \Rightarrow a_1 \preceq^{\sim} a_2$

Proposition: $abs(c_1) \preceq^{\sim} abs(c_2) \Rightarrow c_1 \preceq c_2$

- ▶ subtyping: $a_1 <:\sim a_2 \Leftrightarrow (a_1 \bowtie^{\sim} a_2) \wedge (a_1 \preceq^{\sim} a_2)$

\Rightarrow can be checked in constant time.

Proofs in Coq

$$\text{concr } ((b^0, b^1, r)) \stackrel{\text{def}}{\Leftrightarrow} \left\{ w, \forall i \geq 1, \quad \wedge \quad \begin{array}{l} w[i] = 1 \Rightarrow \mathcal{O}_w(i-1) < r \times i + b^1 \\ w[i] = 0 \Rightarrow \mathcal{O}_w(i-1) \geq r \times i + b^0 \end{array} \right\}$$

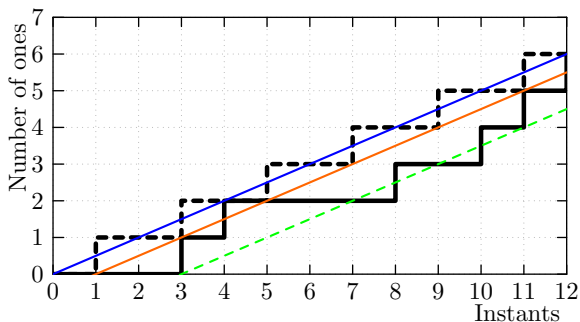
```
Definition in_abstractionj (w: ibw) (a:abstractionj) :=  
  forall i: nat,  
  forall H_i_ge_1: (i >= 1)%nat,  
  (w i = true -> ones w (pred i) < absj_r a * i + absj_b1 a)  
  /\  
  (w i = false -> ones w (pred i) >= absj_r a * i + absj_b0 a).
```

Proofs in Coq

For instance, $on \sim$ correctness:

```
Property on_correctness:
  forall (w1:ibw) (w2:ibw),
  forall H_wf_w1: well_formed_ibw w1,
  forall H_wf_w2: well_formed_ibw w2,
  forall (a1:abstractionj) (a2:abstractionj),
  forall H_wf_a1: well_formed_abstractionj a1,
  forall H_wf_a2: well_formed_abstractionj a2,
  forall (H_a1_eq_absj_w1: in_abstractionj w1 a1),
  forall (H_a2_eq_absj_w2: in_abstractionj w2 a2),
  in_abstractionj (on w1 w2) (on_absj a1 a2).
```

Other Applications - Modelizing Execution Time



$$f :: \forall \alpha. \alpha \text{ on}^{\sim} (0, 0, \frac{1}{2}) \rightarrow \alpha \text{ on}^{\sim} (-\frac{3}{2}, -\frac{1}{2}, \frac{1}{2})$$

Composed twice:

$$f \circ f :: \forall \alpha. \alpha \text{ on}^{\sim} (0, 0, \frac{1}{2}) \rightarrow \alpha \text{ on}^{\sim} (-\frac{6}{2}, -\frac{2}{2}, \frac{1}{2})$$

Other Applications

Modeling Several Reads (or writes) at the Same Instant

