

Relational interprocedural analysis for concurrent programs

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Outline

Introduction

Challenge: combining recursion and concurrency

Existing approaches

Our approach

Program model and semantics

Instrumenting the standard semantics

Concurrent stack abstraction

Two sources of inspiration

Concurrent stack abstraction

Combining stack and data abstraction

Evaluating the precision of stack abstraction

Analysis of concurrent and recursive programs

Why ?

- ▶ Verification of SystemC/TLM model
- ▶ Viewed as programs with
 - ▶ concurrency (static multithreading)
 - ▶ interacting threads specified with procedures

Analysis of concurrent and recursive programs

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- ▶ Verification of SystemC/TLM model
- ▶ Viewed as programs with
 - ▶ concurrency (static multithreading)
 - ▶ interacting threads specified with procedures

Why not ?

- ▶ Strong undecidability result

Sequential versus concurrent program analysis

Analysis of concurrent programs without recursion

- ▶ Exact analysis computable for Boolean programs

Interprocedural analysis of sequential programs

- ▶ Well-understood [CC77, SP81, KS92]
- ▶ Exact analysis computable for Boolean programs
 - ▶ Not only w.r.t. properties/invariants
 - ▶ But also w.r.t. full call-stacks

Interprocedural analysis of concurrent programs ?

- ▶ Active research, several and incomparable solutions
- ▶ Exact analysis **not computable** even for Boolean programs ([Ram00]: reduction to the Post problem)

Challenge: combination of recursion and concurrency I

Sequential recursive programs

1. Computing the denotational semantics of procedures
 - ▶ block (procedure) $P_i \leftrightarrow$ predicate transformer $\phi_i : \mathcal{P} \rightarrow \mathcal{P}$
 - ▶ The ϕ_i 's are expressed in function of each other
 - ▶ Fixpoint equation in the domain $\mathcal{P} \rightarrow \mathcal{P}$
2. Propagating reachability information
 - ▶ Propagation of input predicate using the ϕ_i 's for procedure calls (in the domain \mathcal{P})

The two steps can be combined in one single step (predicate transformers or **summaries** specialized on reachable input)

Concurrent single-procedure programs

- ▶ Reduces to a sequential program by interleaved product of control-flow graphs.

Challenge: combination of recursion and concurrency II

Requirement for the combination

1. Model accurately procedure call and return semantics in each thread
2. Take into account modifications of global variables made by the other threads.

Evaluation criteria

- ▶ static or dynamic thread creation
- ▶ communications between threads ?
 - ▶ with `parbegin` construct, no communication *during* thread execution
 - ▶ with shared global variables, any synchronisation mechanism
- ▶ local variables in procedures ?
less easier than global variables, because of their temporary lifetime

Thread-modular model-checking [FQ03]

Principle

- ▶ To each thread t :
 - ▶ $R(t, g, l)$: true if global and local store is reachable;
 - ▶ $G(t, g, g')$: true if a step of t can go from g to g' .
- ▶ Inference rules

$$\frac{R(t, g, l) \quad G(e, g, g') \quad t \neq e}{R(t, g', l)} \quad \frac{R(t, g, l) \quad \mathcal{T}(t, g, l, g', l')}{R(t, g', l') \quad G(t, g, g')}$$

Generalization to recursive programs: explicit stack for each thread

Pros/Cons

- + Efficiency (the initial goal)
- Termination non-guaranteed, unless further abstraction
- Precision: abstracts the local stores, ignores the order of steps performed by the environment

Other approaches I

Identifying transactions [QRR04]

- ▶ Notion of transaction and transaction boundaries
- ▶ Boundaries may not match procedure calls and returns
- ▶ Subclass for which termination is guaranteed

Analysis under a context bound

- ▶ Considers only executions with a bounded number of context switches
 - ▶ unbounded number of steps possible between switches
- ▶ Allows reduction to sequential analysis
- ▶ More reminiscent of symbolic execution
 - ▶ discovers bugs, does not prove properties

Other approaches II

Alternative representations of the state-space

- ▶ Regular model-checking techniques (Bouajjani, Esparza, Touili)
 - ▶ Concurrent programs: set of PDA
 - ▶ Various abstractions applied to their stacks
- ▶ Rewriting techniques: SPADE tool (Sighireanu, Touili)
 - ▶ Represents states with terms and uses rewriting techniques (Timbuk tree automata library of Thomas Genet)
 - ▶ Handles dynamic thread creation

Not easily combined with infinite data abstraction, like convex polyhedra.

Our approach

State-space of 2-threads programs

$$S = \begin{array}{c} \text{GEnv} \\ \text{global store} \end{array} \times \begin{array}{c} (K^1 \times \text{LEnv}^1)^+ \\ \text{stack of thread 1} \end{array} \times \begin{array}{c} (K^2 \times \text{LEnv}^2)^+ \\ \text{stack of thread 2} \end{array}$$

We instrument the standard semantics

properties on **executions** \implies properties on reachable **states**

$$S_i = (K^1 \times \text{Env}^1)^+ \times (K^2 \times \text{Env}^2)^+$$

We abstract stacks into sets

$$\wp\left((K^1 \times \text{Env}^1)^+ \times (K^2 \times \text{Env}^2)^+ \right) \\ \iff \\ \wp(K^1 \times K^2 \times \text{Env}^1 \times \text{Env}^2) \times \wp(K^1 \times \text{Env}^1) \times \wp(K^2 \times \text{Env}^2) \\ \begin{array}{ccc} \text{pairs of stack tops} & \times & \text{stack tails 1} \end{array} \times \begin{array}{c} \text{stack tails 2} \end{array}$$

Defines the analysis method

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Program example: synchronisation barriers

```
var go : bool,
    counter,p0,p1 : int;

initial counter==0 and go;

proc barrier(lgo:bool)
  returns (nlgo:bool)
begin
  lgo = not lgo;
  counter = counter+1;
  if counter==2 then
    counter=0; go = lgo;
  else
    assume(lgo==go);
  endif;
  nlgo = lgo;
end

thread T0:
var lgo0:bool;
begin
  p0 = 0; lgo0 = true;
  while p0<=5 do
    lgo0 = barrier(lgo0);
    p0 = p0 + 1;
  done;
end

thread T1:
var lgo1:bool;
begin
  p1 = 0; lgo1 = true;
  while p1<=10 do
    lgo1 = barrier(lgo1);
    p1 = p1 + 1;
  done;
  fail;
end
```

Program model I

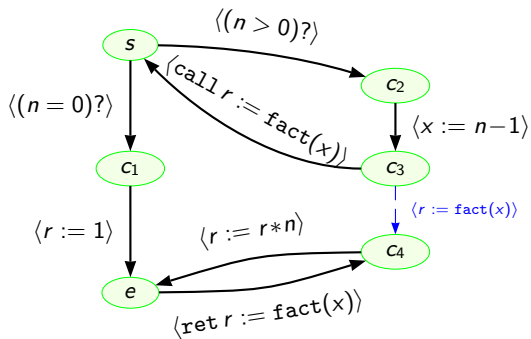
Programs composed of

- ▶ global variables \mathbf{g}
- ▶ procedures
 - ▶ $\mathbf{fpi}, \mathbf{fpo}$: formal input/output parameters of P
 - ▶ \mathbf{l} : local variables of P
- ▶ static threads communicating via global variables
 - ▶ P_0^t : main procedure of thread t

Program model II

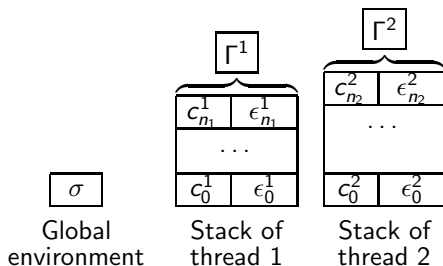
Global control flow graph G

- ▶ control points K
- ▶ s_j, e_j : start and exit points of procedure P_j
- ▶ Edges labeled with instructions



Program semantics I

Program state



with $\sigma \in \text{GEnv} = \text{GVar} \rightarrow \text{Value}$: global environments
 $\epsilon \in \text{LEnv} = \text{LVar} \rightarrow \text{Value}$: local environments

Program semantics II

Operational Semantics

$$\frac{\begin{array}{l} I(c, c') = \langle R \rangle \\ R(\sigma, \epsilon, \sigma', \epsilon') \end{array}}{\langle \sigma, \Gamma \cdot \langle c, \epsilon \rangle, \Gamma^2 \rangle \rightarrow \langle \sigma', \Gamma \cdot \langle c', \epsilon' \rangle, \Gamma^2 \rangle} \quad (\text{Intra})$$

$$\frac{\begin{array}{l} I(c, s_j) = \langle \text{call } \mathbf{y} := P_j(\mathbf{x}) \rangle \\ R_{\mathbf{y}:=P_j(\mathbf{x})}^+(\sigma, \epsilon, \epsilon_j) \end{array}}{\langle \sigma, \Gamma \cdot \langle c, \epsilon \rangle, \Gamma^2 \rangle \rightarrow \langle \sigma, \Gamma \cdot \langle c, \epsilon \rangle \cdot \langle s_j, \epsilon_j \rangle, \Gamma^2 \rangle} \quad (\text{Call})$$

$$\frac{\begin{array}{l} I(e_j, c) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle \\ R_{\mathbf{y}:=P_j(\mathbf{x})}^-(\sigma, \epsilon, \epsilon_j, \sigma', \epsilon') \end{array}}{\langle \sigma, \Gamma \cdot \langle \text{call}(c), \epsilon \rangle \cdot \langle e_j, \epsilon_j \rangle, \Gamma^2 \rangle \rightarrow \langle \sigma', \Gamma \cdot \langle c, \epsilon' \rangle, \Gamma^2 \rangle} \quad (\text{Ret})$$

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Instrumenting the semantics I

Principle

- ▶ Global variables pushed on stacks
- ▶ Formal input parameters get a frozen copy
- ▶ New thread environments $\epsilon(\mathbf{g}_0, \mathbf{fpi}_0, \mathbf{g}, \mathbf{l})$
 - ▶ $\mathbf{g}_0, \mathbf{fpi}_0$: values of global and parameters at start point
 - ▶ \mathbf{g}, \mathbf{l} : values of global and local variables at current point

$\mathbf{g}_0, \mathbf{fpi}_0$ tags environments with (an abstraction of) the call-context

State-space $S_i = (K^1 \times \text{Env}^1)^+ \times (K^2 \times \text{Env}^2)^+$

Threads must agree on the current value of global variables

Instrumenting the semantics II

Instrumented semantics: thread in isolation

$$\frac{I(c, c') = \langle R \rangle \quad R(\epsilon, \epsilon') \wedge \epsilon(\mathbf{g}_0, \mathbf{fpi}_0) = \epsilon'(\mathbf{g}_0, \mathbf{fpi}_0)}{\Gamma \cdot \langle c, \epsilon \rangle \rightarrow_i^t \Gamma \cdot \langle c', \epsilon' \rangle} \quad (\text{IntraF})$$

$$\frac{I(c, s_j) = \langle \text{call } \mathbf{y} := P_j(\mathbf{x}) \rangle \quad R_{\mathbf{y}:=P_j(\mathbf{x})}^+(\epsilon, \epsilon_j)}{\Gamma \cdot \langle c, \epsilon \rangle \rightarrow_i^t \Gamma \cdot \langle c, \epsilon \rangle \cdot \langle s_j, \epsilon_j \rangle} \quad (\text{CallF})$$

$$\frac{I(e_j, c) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle \quad R_{\mathbf{y}:=P_j(\mathbf{x})}^-(\epsilon, \epsilon_j, \epsilon')}{\Gamma \cdot \langle \text{call}(c), \epsilon \rangle \cdot \langle e_j, \epsilon_j \rangle \rightarrow_i^t \Gamma \cdot \langle c, \epsilon' \rangle} \quad (\text{RetF})$$

Instrumenting the semantics III

Instrumented semantics: full program

Notifying update of global variables to other threads

$$\frac{\Gamma_1 \rightarrow_i^1 \Gamma'_1 \quad \Gamma'_1 = \Gamma''_1 \cdot \langle c'_1, \epsilon'_1 \rangle \quad \epsilon'_2 = \epsilon_2[\mathbf{g} \mapsto \epsilon'_1(\mathbf{g})]}{\langle \Gamma_1, \Gamma_2 \cdot \langle c_2, \epsilon_2 \rangle \rangle \rightarrow_i \langle \Gamma'_1, \Gamma_2 \cdot \langle c_2, \epsilon'_2 \rangle \rangle} \quad (\text{Conc1F})$$

$$\dots \quad (\text{Conc2F})$$

Properties of reachable stacks in instrumented semantics

Definition

A stack $\Gamma = \langle c_0, \epsilon_0 \rangle \dots \langle c_n, \epsilon_n \rangle \in \text{Act}^+$ is *well-formed* if:

- (i) c_j is a call site for the procedure $\text{Proc}(c_{j+1}) = P_j$
- (ii) equality between actual and formal input parameters holds:
 $\epsilon_j(\mathbf{g}, \mathbf{x}) = \epsilon_{j+1}(\mathbf{g}_0, \mathbf{fpi}_0^j)$.

A state $\langle \Gamma^1, \Gamma^2 \rangle \in S_i$ is *well-formed* if Γ^1 and Γ^2 are well-formed

Theorem

Reachable states are well-formed

Strong condition for an activation record to lie below another activation record in stacks

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Relational interprocedural analysis of sequential prog. I

formalized as a stack abstraction on instrumented semantics [JS04]

Galois connection $S_i = \wp(\text{Act}^+) \xrightleftharpoons[\alpha_f]{\gamma_f} \wp(\text{Act}) \times \wp(\text{Act})$ with

$$\alpha_f : \{\Gamma = r_0 \dots r_n\} \mapsto \left\langle \begin{array}{c} \text{hd}(\Gamma), \\ \text{tl}(\Gamma) \end{array} \right\rangle = \left\langle \begin{array}{c} \{r_n\}, \\ \{r_i \mid 0 \leq i < n\} \end{array} \right\rangle$$

$$\gamma_f : \langle Y_{\text{hd}}, Y_{\text{tl}} \rangle \mapsto \left\{ \Gamma = r_0 \dots r_n \left| \begin{array}{l} r_n \in Y_{\text{hd}} \\ \forall 0 \leq i < n : r_i \in Y_{\text{tl}} \\ \Gamma \text{ is a well-formed stack} \end{array} \right. \right\}$$

Relational interprocedural analysis of sequential prog. II

Induced abstract semantics: for procedure return

$$\text{instr}(e_j, \text{ret}(c)) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle$$

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$$\text{instr}(e_j, \text{ret}(c)) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle$$

$$Y_{\text{tl}}[c](\mathbf{g}_0, \mathbf{fpi}_0, \mathbf{g}, l) \qquad Y_{\text{hd}}[e_j](\mathbf{g}_0^j, \mathbf{fpi}_0^j, \mathbf{g}', l')$$

(tail) (top)

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(tail) (top)

$$(\mathbf{g}, \mathbf{x}) = (\mathbf{g}_0^j, \mathbf{fpi}_0^j)$$

(well-formedness condition)

$$R_{\mathbf{y}:=P_j(\mathbf{x})}^-(\mathbf{g}, \mathbf{l}, \mathbf{g}', \mathbf{l}', \mathbf{g}'', \mathbf{l}'')$$

(output parameter passing)

Relational interprocedural analysis of sequential prog. II

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$$Y_{\text{hd}}[\text{ret}(c)](\mathbf{g}_0^j, \mathbf{fpi}_0^j, \mathbf{g}'', \mathbf{l}'')$$

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(output parameter passing)

$$Y_{\text{hd}}[\text{ret}(c)](\mathbf{g}_0^j, \mathbf{fpi}_0^j, \mathbf{g}'', \mathbf{l}'')$$

Theorem (Optimality)

The abstract semantics preserves stack tops (but not full stacks)

Analysis of non-recursive concurrent prog. I

State-space: $S = \text{GEnv} \times (K^1 \times \text{LEnv}^1) \times (K^2 \times \text{LEnv}^2)$

Principle

- ▶ One observes $\wp(S) \simeq K^1 \times K^2 \rightarrow \wp(\text{GEnv} \times \text{LEnv}^1 \times \text{LEnv}^2)$
- ▶ If needed, one abstracts it with $K^1 \times K^2 \rightarrow \text{Env}^\sharp$
 - ▶ Env^\sharp : abstract environments (octagons, convex polyhedra...)

Ability to relate the local environments of different threads is fundamental !

Analysis of non-recursive concurrent prog. II

Example

- ▶ Two predicates $Y^1(g, l^1) = (g = l^1)$ and $Y^2(g, l^2) = (g = l^2 - 1)$
 - ▶ Thread 1 executes $g := g + l^1$
 - ▶ Effect on Y^1 : $(g = 2l^1)$
 - ▶ Effect on Y^2 ?
 - ▶ build $Y = Y^1 \wedge Y^2 = (g = l^1 \wedge l^1 = l^2 - 1)$
 - ▶ compute the effect of the instruction on Y
 - ▶ forget the variable l^1
- $\implies (g = 2l^2 - 2)$

Need for relate at least temporarily local environments of different threads

Analysis of non-recursive concurrent prog. III

Example

```
thread T1:          thread T2:
var i:int;         var j:int;
begin
  i = 0;           j = 0;
  while i<=10 do   while j<=11 do
    sync a;        sync a;
    i = i+1;       j = j+1;
  done             done
end               end
```

To establish that the thread 2 does not terminate we need to infer the invariant $i = j$ just after the synchronisation

Need for relate as long as possible local environments of different threads

Concurrent stack abstraction I

Galois connection

$\wp(\text{Act}^+ \times \text{Act}^+) \xrightleftharpoons[\alpha_c]{\gamma_c} \wp(\text{Act} \times \text{Act}) \times \wp(\text{Act}) \times \wp(\text{Act})$ with

$$\alpha_c\left(\left\{\left\langle \overbrace{r_0^1 \dots r_{n_1}^1}^{\Gamma_1}, \overbrace{r_0^2 \dots r_{n_2}^2}^{\Gamma_2} \right\rangle\right\}\right) = \left\langle \begin{array}{l} \text{hd}(\Gamma_1, \Gamma_2), \\ \text{tl}(\Gamma_1), \\ \text{tl}(\Gamma_2) \end{array} \right\rangle = \left\langle \begin{array}{l} \{\langle r_{n_1}^1, r_{n_2}^2 \rangle\}, \\ \{r_{i_1}^1 \mid 0 \leq i_1 < n_1\}, \\ \{r_{i_2}^2 \mid 0 \leq i_2 < n_2\} \end{array} \right\rangle$$

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$$\gamma_c\left(\langle Y_{\text{hd}}, Y_{\text{tl}}^1, Y_{\text{tl}}^2 \rangle\right) = \left\{ \left\langle r_0^1 \dots r_{n_1}^1, r_0^2 \dots r_{n_2}^2 \right\rangle \mid \begin{array}{l} \langle r_{n_1}^1, r_{n_2}^2 \rangle \in Y_{\text{hd}} \wedge \epsilon_{n_1}^1(\mathbf{g}) = \epsilon_{n_2}^2(\mathbf{g}) \\ \forall 0 \leq i_1 < n_1 : r_{i_1}^1 \in Y_{\text{tl}}^1 \\ \forall 0 \leq i_2 < n_2 : r_{i_2}^2 \in Y_{\text{tl}}^2 \\ r_0^1 \dots r_{n_1}^1 \text{ and } r_0^2 \dots r_{n_2}^2 \text{ are well-formed stacks} \end{array} \right\}$$

Concurrent stack abstraction I

Galois connection

$\wp(\text{Act}^+ \times \text{Act}^+) \xrightleftharpoons[\alpha_c]{\gamma_c} \wp(\text{Act} \times \text{Act}) \times \wp(\text{Act}) \times \wp(\text{Act})$ with

$$\alpha_c\left(\left\langle \overbrace{\langle r_0^1 \dots r_{n_1}^1 \rangle}^{\Gamma_1}, \overbrace{\langle r_0^2 \dots r_{n_2}^2 \rangle}^{\Gamma_2} \right\rangle\right) = \left\langle \begin{array}{c} \text{hd}(\Gamma_1, \Gamma_2), \\ \text{tl}(\Gamma_1), \\ \text{tl}(\Gamma_2) \end{array} \right\rangle = \left\langle \begin{array}{c} \{\langle r_{n_1}^1, r_{n_2}^2 \rangle\}, \\ \{r_{i_1}^1 \mid 0 \leq i_1 < n_1\}, \\ \{r_{i_2}^2 \mid 0 \leq i_2 < n_2\} \end{array} \right\rangle$$

$$\gamma_c\left(\langle Y_{\text{hd}}, Y_{\text{tl}}^1, Y_{\text{tl}}^2 \rangle\right) = \left\{ \left\langle r_0^1 \dots r_{n_1}^1, r_0^2 \dots r_{n_2}^2 \right\rangle \mid \begin{array}{l} \langle r_{n_1}^1, r_{n_2}^2 \rangle \in Y_{\text{hd}} \wedge \epsilon_{n_1}^1(\mathbf{g}) = \epsilon_{n_2}^2(\mathbf{g}) \\ \forall 0 \leq i_1 < n_1 : r_{i_1}^1 \in Y_{\text{tl}}^1 \\ \forall 0 \leq i_2 < n_2 : r_{i_2}^2 \in Y_{\text{tl}}^2 \\ r_0^1 \dots r_{n_1}^1 \text{ and } r_0^2 \dots r_{n_2}^2 \text{ are well-formed stacks} \end{array} \right\}$$

Abstract semantics: induced mechanically by abstracting the instrumented semantics with the Galois connection

Concurrent stack abstraction II

Predicate version

$$A_c \simeq (K^1 \times K^2 \rightarrow \wp(\text{Env}^1 \times \text{Env}^2)) \times (K^1 \rightarrow \wp(\text{Env}^1)) \times (K^2 \rightarrow \wp(\text{Env}^2))$$

An abstract value: $\langle Y_{\text{hd}}, Y_{\text{tl}}^1, Y_{\text{tl}}^2 \rangle$ with

▶ $Y_{\text{hd}}[c^1, c^2](\mathbf{g}_0^1, \mathbf{fpi}_0^1, \mathbf{g}_0^2, \mathbf{fpi}_0^2, \mathbf{g}, \mathbf{l}^1, \mathbf{l}^2)$

Top environments directly related

▶ $Y_{\text{tl}}^1[c^1](\mathbf{g}_0^1, \mathbf{fpi}_0^1, \mathbf{g}, \mathbf{l}^1)$

$Y_{\text{tl}}^2[c^2](\mathbf{g}_0^2, \mathbf{fpi}_0^2, \mathbf{g}, \mathbf{l}^2)$

Tail environments indirectly related via global variables

Abstract postcondition relies on

1. conjunctions and disjunctions
2. equality constraints between variables/dimensions
3. existential quantification
4. relations induced by intraprocedural instructions $\langle R \rangle$

Concurrent stack abstraction III

Induced semantics for procedure return (in thread 1)

$$\text{instr}(e_j, \text{ret}(c)) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle$$

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$$\text{instr}(e_j, \text{ret}(c)) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle$$

$$Y_{\text{tl}}^1[c, c^2](\mathbf{g}_0, \mathbf{fpi}_0, \mathbf{g}, l) \quad Y_{\text{hd}}[e_j, c^2](\mathbf{g}_0^j, \mathbf{fpi}_0^j, \mathbf{g}_0^2, \mathbf{fpi}_0^2, \mathbf{g}', l', l^2)$$

(tail 1) (top)

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Induced semantics for procedure return (in thread 1)

$$\text{instr}(e_j, \text{ret}(c)) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle$$

$$Y_{\text{tl}}^1[c, c^2](\mathbf{g}_0, \mathbf{fpi}_0, \mathbf{g}, l) \quad Y_{\text{hd}}[e_j, c^2](\mathbf{g}_0^j, \mathbf{fpi}_0^j, \mathbf{g}_0^2, \mathbf{fpi}_0^2, \mathbf{g}', l', l^2)$$

(tail 1) (top)

$$(\mathbf{g}, \mathbf{x}) = (\mathbf{g}_0^j, \mathbf{fpi}_0^j)$$

(well-formedness condition for stack 1)

Concurrent stack abstraction III

Induced semantics for procedure return (in thread 1)

$$\text{instr}(e_j, \text{ret}(c)) = \langle \text{ret } \mathbf{y} := P_j(\mathbf{x}) \rangle$$

$$Y_{\text{tl}}^1[c, c^2](\mathbf{g}_0, \mathbf{fpi}_0, \mathbf{g}, l) \quad Y_{\text{hd}}[e_j, c^2](\mathbf{g}_0^j, \mathbf{fpi}_0^j, \mathbf{g}_0^2, \mathbf{fpi}_0^2, \mathbf{g}', l', l^2)$$

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$$R_{\mathbf{y}:=P_j(\mathbf{x})}^-(\mathbf{g}, l, \mathbf{g}', l', \mathbf{g}'', l'')$$

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$$Y_{\text{hd}}[\text{ret}(c)](\mathbf{g}_0^j, \mathbf{fpi}_0^j, \mathbf{g}_0^2, \mathbf{fpi}_0^2, \mathbf{g}'', l'', l^2)$$

Concurrent stack abstraction IV

Optimality result: concurrent stack abstraction reduces

1. to classical interprocedural analysis
(one thread with recursion)
2. to classical analysis of concurrent programs
(multiple threads without recursion)

In the other cases, it induces approximations.

- ▶ Otherwise it would contradict the undecidability result

Outline

Introduction

Challenge: combining recursion and concurrency

Existing approaches

Our approach

Program model and semantics

Instrumenting the standard semantics

Concurrent stack abstraction

Two sources of inspiration

Concurrent stack abstraction

Combining stack and data abstraction

Evaluating the precision of stack abstraction

Combining stack and data abstraction

We assume that we are given a Galois connection

$$\wp(\text{Env}) \begin{array}{c} \xleftarrow{\gamma_e} \\ \xrightarrow{\alpha_e} \end{array} \text{Env}^\sharp$$

Then we can combine Galois connections:

$$\begin{array}{l} \wp\left(\left(K^1 \times \text{Env}\right)^+ \times \left(K^2 \times \text{Env}\right)^+\right) \\ \xleftrightarrow[\alpha_c]{\gamma_c} \overbrace{\left(K^1 \times K^2 \rightarrow \wp(\text{Env})\right) \times \left(K^1 \rightarrow \wp(\text{Env})\right) \times \left(K^2 \rightarrow \wp(\text{Env})\right)}^{A_c} \\ \xleftrightarrow[\alpha_e]{\gamma_e} \overbrace{\left(K^1 \times K^2 \rightarrow \text{Env}^\sharp\right) \times \left(K^1 \rightarrow \text{Env}^\sharp\right) \times \left(K^2 \rightarrow \text{Env}^\sharp\right)}^{A_c^\sharp} \end{array}$$

Example of suitable data abstractions

- ▶ **Boolean programs:** $\text{Env} \simeq \mathbb{B}^n$
We have a finite lattice, no need for data abstraction
- ▶ **Programs with numerical variables:** $\text{Env} \simeq \mathbb{R}^n$
Relational abstraction applicable: octagons, convex polyhedra, linear congruences, ...
Approximations: due to \sqcup and to abstraction of single instructions
- ▶ **Programs with either Booleans or pointers to memory cells**
Stores represented/abstracted with 3-valued logical structures [SRW02]:

$$\wp(\text{Env}) \simeq \wp(2 - \text{STRUCT}) \iff \wp(3 - \text{BSTRUCT})$$

Enable the extension of [JLRS04] to concurrent programs

Complexity analysis

Program	single-thread	concurrent
single-procedure	$k \cdot \varphi(g + l)$	$k^n \cdot \varphi(g + nl)$
recursion	$2k \cdot \varphi(2g + l)$	$k^n \cdot \varphi(g + n(g + l))$

n : number of threads

g : number of global variables

k : number of control points

l : number of local variables

$\varphi(d)$: complexity of d -dimensional environments

Assuming $\varphi(d) = \mathcal{O}(2^d)$, the **global complexity** is

- ▶ polynomial in the size k of the CFGs,
- ▶ exponential in the number n of threads,
- ▶ in $\mathcal{O}(\phi(nd))$ if $d = g + l$ is the number of visible variables active in each thread

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Well-known techniques for reducing the complexity can be reused: partial order and symmetry reduction for concurrency, Cartesian product and/or variables packing for data abstraction, ...

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Implementation

- ▶ CONCURINTERPROC: INTERPROC generalized with concurrency
- ▶ Programs with finite-state and/or numerical variables
Data abstraction: $\wp(\mathbb{B}^n \times \mathbb{R}^p) \iff \mathbb{B}^n \rightarrow \text{Pol}(\mathbb{R}^p)$
implemented with MTBDDs (CUDD & APRON)
- ▶ Both forward and backward analysis implemented
- ▶ Choice between preemptive and cooperative scheduling

Online version available at

<http://pop-art.inrialpes.fr/interproc/concurinterprocweb.cgi>

Experiments

Goal

- ▶ Illustrate the precision of our method
- ▶ Analyze some of the approximations it induces

Test programs

Synchronisation algorithms requiring a detailed analysis of interactions between threads

- ▶ Mutual exclusion algorithms: Peterson, Kessel
- ▶ Barrier synchronisation: a protocol using counters

Mutual exclusion: Peterson

```
var b0,b1,turn:bool;
initial not b0 and not b1;

proc acquire(tid:bool) returns ()
begin
  if not tid then
    b0 = true; turn = tid;
    assume (b1==false or turn==not tid);
  else
    b1 = true; turn = tid;
    assume (b0==false or turn==not tid);
  endif;
end

proc release(tid:bool) returns ()
begin
  if not tid
  then b0 = false;
  else b1 = false; endif;
end
```

```
proc main(tid:bool) returns ()
begin
  while true do
    acquire(tid); /* C */
    release(tid);
  done;
end

thread T0:
var tid:bool;
begin
  tid = false; main(tid);
end

thread T1:
var tid:bool;
begin
  tid = true; main(tid);
end
```

Non-terminating example of [QRR04]

```
var .., g:uint[3], x,y:bool, ...;
initial .. and
    g==uint[3](0) and not x and not y,
proc foo(tid:bool,q:bool) returns ()
begin
    if not q then
        x=true; y=true; foo(tid,q);
    else
        acquire(tid);
        g = g + uint[3](1);
        release(tid);
    endif;
end
proc main(tid:bool) returns ()
var q:bool;
begin
    q = random;
    foo(tid,q);
    acquire(tid);
    if g==uint[3](0) then fail; endif;
    release(tid);
end
```

```
thread T0:
var tid:bool;
begin
    tid = false; main(tid);
end

thread T1:
var tid:bool;
begin
    tid = true; main(tid);
end
```

Synchronisation barriers with counters

```
var go : bool,  
    counter,p0,p1 : int,  
initial counter==0 and go;  
  
proc barrier(lgo:bool)  
    returns (nlgo:bool)  
begin  
    lgo = not lgo;  
    counter = counter+1;  
    if counter==2 then  
        counter=0; go = lgo;  
    else  
        assume(lgo==go);  
    endif;  
    nlgo = lgo;  
end
```

```
thread T0:  
var lgo0:bool;  
begin  
    p0 = 0; lgo0 = true;  
    while p0<=5 do  
        lgo0 = barrier(lgo0);  
        p0 = p0 + 1;  
    done;  
end  
thread T1:  
var lgo1:bool;  
begin  
    p1 = 0; lgo1 = true;  
    while p1<=10 do  
        lgo1 = barrier(lgo1);  
        p1 = p1 + 1;  
    done;  
    fail;  
end
```

Success if p1,p2 global, failure if they are local

Synchronisation barrier with counters: half-working

```
var go : bool, counter:int;
initial counter==0 and go;
proc barrier(lgo:bool) returns (nlgo:bool)
begin
  lgo = not lgo; counter = counter+1;
  if counter==2
  then counter=0; go = lgo;
  else assume(lgo==go); endif;
  nlgo = lgo;
end                                     /* E0 */

thread T0:
var lgo0:bool;
begin
  lgo0 = true;                          /* A0 */
  lgo0 = barrier(lgo0); /* A1 */
/*lgo0 = barrier(lgo0); /* A2 */ */
end

thread T1:
var lgo1:bool;
begin
  lgo1 = true;                          /* B0 */
  lgo1 = barrier(lgo1); /* B1 */
  lgo1 = barrier(lgo1); /* B2 */
  lgo1 = barrier(lgo1); /* B3 */
  fail;
end
```

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 - ▶ Abstract semantics derived mechanically
 - ▶ Most technical aspect
- ▶ Separates control and data abstraction
 - ▶ Can extend any relational interprocedural analysis to concurrent programs
- ▶ Experimental evaluation of its precision
 - ▶ Local variables handled less precisely than global variables
 - ▶ Hence, procedure inlining may improve precision

Perspective

Stack abstraction

- ▶ Better precision/Less modularity/Worse complexity: adding more information in the call-context for each thread ?
- ▶ Further abstraction to derive thread-modular analysis of [FQ03] ?
- ▶ Adapting iteration and widening techniques to a concurrent context

Application to TLM models

- ▶ Exploiting cooperative scheduling
- ▶ Encoding of TLM synchronisation concepts (events, time)
- ▶ Built-in, higher-level synchronisation primitives ?



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